



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of)
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ITO)
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Application Number: 10/770,479)
)
Filed: February 4, 2004)
)
For: DESIGN METHOD AND APPARATUS FOR A)
SEMICONDUCTOR INTEGRATED CIRCUIT)
COMPRISING CHECKERS VERIFYING THE)
INTERFACE BETWEEN CIRCUIT BLOCKS)
(As Amended))
)
Attorney Docket No. NITT.0189)

Art Unit 2825

Examiner
Thompson, Annette M.

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE AND AMENDMENT UNDER 37 C.F.R. §1.111

Sir:

This is in response to the Office Action dated December 30, 2005 in the above identified application, the period for response to which is set to expire on March 30, 2005, with a one-month extension of time. Please amend the above identified application as follows:

03/31/2006 MBIZUNES 00000094 10770479

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